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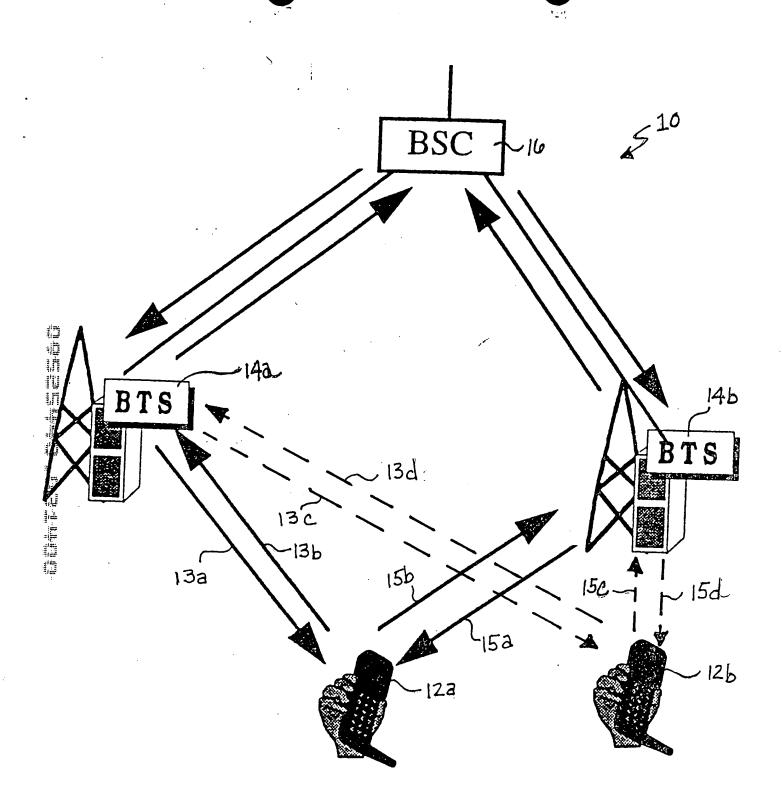


FIG. 1

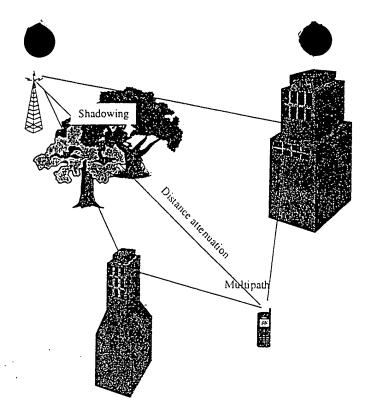
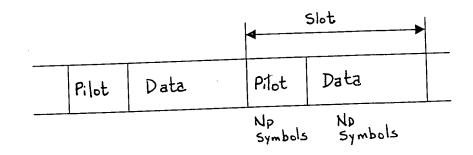


FIG. 2



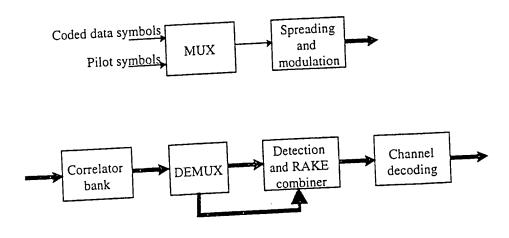


FIG. 3

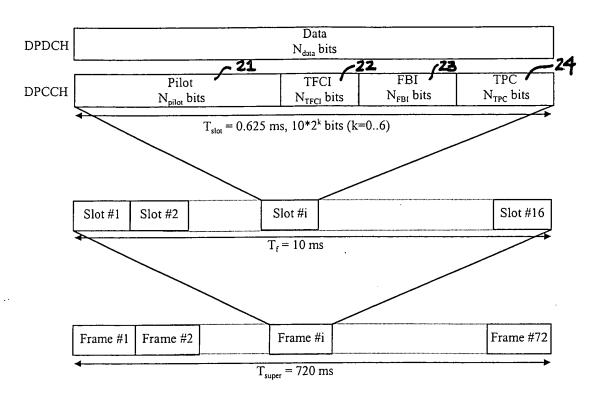


FIG. 4

Channel Bit	Channel Symbol	SF	Bits/	Bits/	N _{pilot}	N _{TPC}	N _{TFCI}	N _{FBI}
Rate (kbps)	Rate (ksps)		Frame	Slot				
16	16	256	160	10	6	2	2	0
16	16	256	160	10	8	2	0	0
16	16	256	160	10	5	2	2	1
16	16	256	160	10	7	2	0	1
16	16	256	160	10	[6]	[2]	[0]	[2]
16	16	256	160	10	[5]	[1]	[2]	[2]

FIG.5

		N_{pilot}	= 6					N _{pilot}	= 8			
Bit#	0	1 2	3	4 5	0	i.;	2	3	4	5	6	7
Slot #1	1	1 1 1	1	1_{a_0} 1_{A_0}	. 1	,1	1	1	1	1 :	1	. 1
2	1	1 1	l	0 1-	1	1	l	1	1	0	I	i.
3	1	0 1	1	:0 , 1,	1	0	1	1	1	. 0	1	1
4	1	1 0	1	0 1	1	1.	1	0	1	0 ;	1	1.
5	1	1. 0	1	i T	1	1	1	0	1	1.	1	1
6	1	1 0	1	1 1	1	1	1	0 ,	1	1	1	1.1
7	1	ô i	1	0 0	1	-0	1	-1	1	0 (1	0
8	1	1 ,0	l	.0 1	l	1 :	I	.0	l	0	1	.1
9	1	1	1	00	1	1	1	123	1	0]	1	ૢૼ૽
10	1	0 1	1	0.4 . 1 .	1	0	1	1.3	1	0	1	131
11	1	1 1	l	1 0	l		1	i	1	: j. 4	1	ő
12	1	0 , , 1	1	0 1	1	. 0 :	1	1	1	0	1	1.
13	1	0. 0	1	0 1	1	0	1	0.	1	0	1	1
14	1	1 0	1	0 . 0	1	.1	1	0	1	0	1	0
15	1	0 1	1	0 0	1	0 -	l	1.	ì	0	1	0
16	ì	0 0	1	0 0	. 1	0	1	0	1	0	1	0

FIG. 6

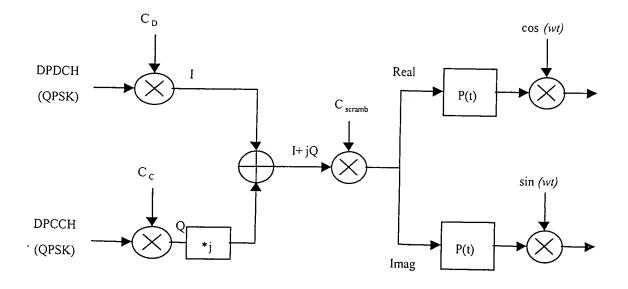


FIG. 7.

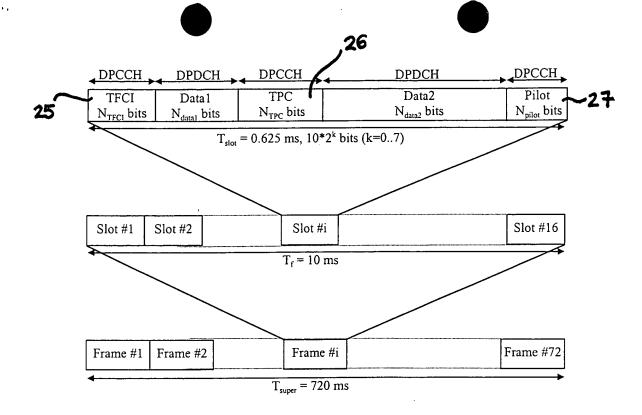
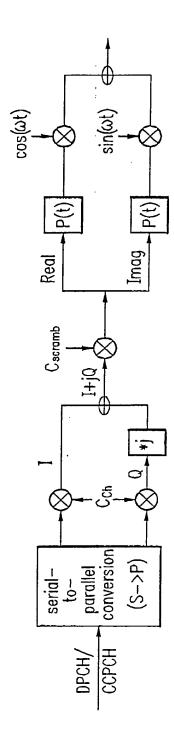


FIG. 8

Symbol rate	8ksps	16,32,64,128ksps	256,512,1024ksps
Symbol #	0 1	0 1 2 3	0 1 2 3 4 5 6 7
Slot # 1	11 11	11 11 11 11	11 412 11 11 11 11 11 11 310
2	11 11	11 11 11 01	11 10 11 10 11 10 11 01
3	11 10	11 01 11 01	11 10 11 01 11 11 11 01
4	11 01	11 10 11 01	11 11 11 01 11 00 11 10
5	11 10	11 10 11 11	11 11 11 00 11 01 11 10
6	11 10	11 10 11 11	11 11 11 11 11 01 11 10
7	11 01	11 01 11 00	11 10 11 11 11 01 11 10
8	11 00	11 10 11 01	11 01 11 00 11 10 11 00
9	11 00	11 11 11 00	11 11 11 10 11 00 11 01
10	11 10	11 01 11 01	11 01 11 11 11 11 11 00
11	11 10	11 11 11 10	11 10 11 10 11 11 11 10
12	11 11	11 01 11 01	11 01 11 10 11 10 11 00
13	11 10	11 00 11 01	11 10 11 01 11 11 10
14	11 11	11 10 11 00	11 00 11 10 11 10 11 00
15	11 00	11 01 11 00	11 01 11 10 11 00 11 00
16	11 00	11 00 11 00	11 10 11 00 11 00 11 00

FIG. 9 5/57

FIG. 10



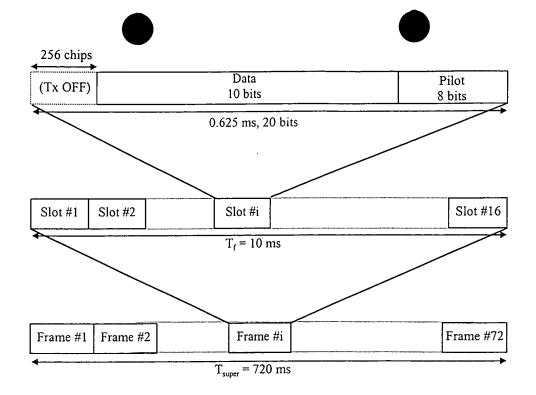


FIG. 11A

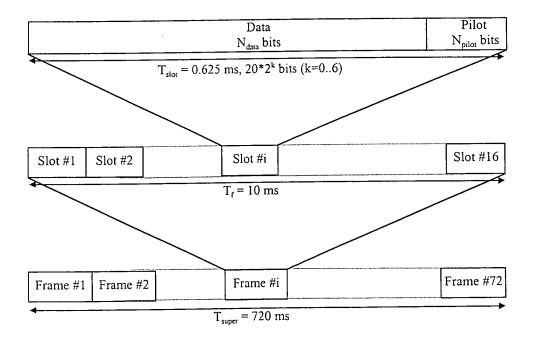


FIG. 11B

	Frame Synchronization Words
Slot Number	1 2 3 4 5L
	$C_1 = (1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 1 \ 1 \ 0 \ 0 \ $
	$C_2 = (1\ 0\ 0\ 0\ 1\ 0\ 1\ 0\ 0\ 1\ 1\ 1\ 0\ 1\ 0\ 1)$
	$C_3 = (1 \ 1 \ 0 \ 1 \ 1 \ 1 \ 0 \ 0 \ 0 \ 1 \ 0 \ 0$
	$C_4 = (0\ 1\ 1\ 1\ 0\ 1\ 1\ 0\ 1\ 0\ 0\ 0\ 1\ 0\ 0\ 1)$
	$C_5 = (1\ 0\ 1\ 1\ 0\ 0\ 0\ 0\ 1\ 0\ 1\ 1\ 1\ 1)$
	$C_6 = (1 \ 1 \ 1 \ 0 \ 0 \ 1 \ 0 \ 1 \ 0 \ 0 \ $
	$C_7 = (0\ 1\ 0\ 0\ 0\ 0\ 1\ 1\ 1\ 0\ 1\ 1\ 1\ 0\ 0)$
	$C_8 = (1 \ 1 \ 1 \ 0 \ 1 \ 0 \ 0 \ 1 \ 0 \ 1 \ 0 \ 1 \ 0)$

FIG. 12A

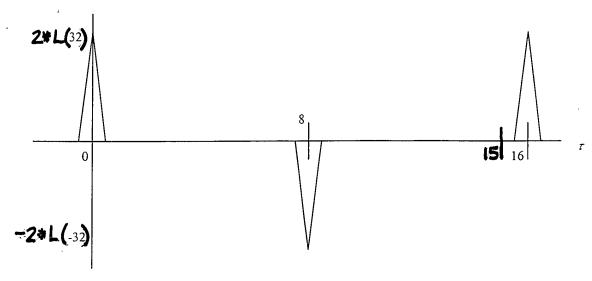
								T			,	r			1	
$R(\tau)$ τ	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
$R_{\rm E}(\tau)$	16	4	0	4	0	-4	0	-4	-16	-4	0	-4	0	4	0	4
$R_{\rm F}(\tau)$	16	-4	0	-4	0	4	0	4	-16	4	0	4	0	-4	0	-4
$R_{\rm G}(\tau)$	16	4	0	-4	0	4	0	-4	-16	-4	0	4	0	-4	0	4
$R_{\rm H}(\tau)$	16	-4	0	4	0	-4	0	4	-16	4	0	-4	0	4	0	-4

R₁

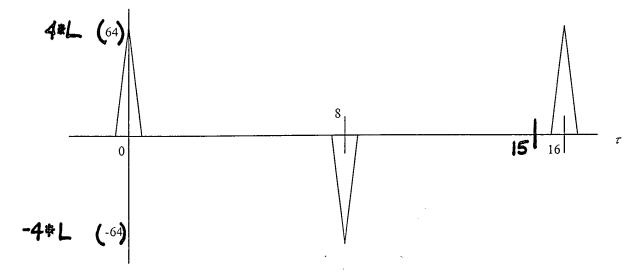
R2

F1G.12B

 $(R_{\rm E}(\tau) + R_{\rm F}(\tau))$, or $(R_{\rm G}(\tau) + R_{\rm H}(\tau))$



$$R_{\rm E}(\tau) + R_{\rm F}(\tau) + R_{\rm G}(\tau) + R_{\rm H}(\tau)$$



F1G. 13B

FIG. 14A

	N	pilo12 =	= 5		N _{pilot2}	= 6	
Bit#	0 1	2	3 4	0	1 2	3	4 57
Slot #1	, n	1	11 0	1	11	1	i o
2	ú 1 0 .,	1	1	1	. J 0	1	0 (1)
3	0.5.70	1	0 1	1	0 0	1	0 . 1
4	l O	1	1	1	11 0	1	1 0
5		1	0.00	1		1	1
6	1 0	1	. [2] [2] [2]	1	1 0	1	$\mathbf{L}^{\tau} = 0$
7	lista 1	1	0 1	1	1 . 1	1	0 , 1
8	11 - 0	1	0 0	1	1 1 0	1	0 0
9	0 0	1	0 1	1	0 0	1	0 1
10	0.00	1	0 0	1	0 1	1	0 20
11	î, i	1	1. 0	1	17. 1	1	1 2.0
12	0 1	1	0.00	1	0 - 1	1	+ 0 1 0
13	0:= 0 v	1	0 1	1	0.4.00	1	0 : 7 2 410 1
14	0. 1	1	0 0	1	0.714.11	1	0 . 50
15	0.4	1	i 0	1	0 0	1	1 4 6
16	0 1	1	15.65% 1	1	0.4.1	1	1 1

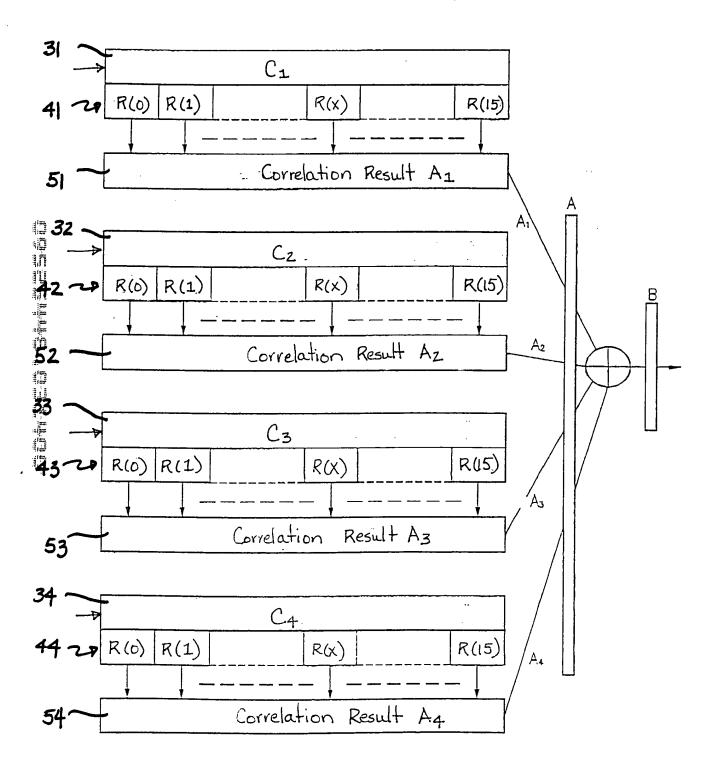
FIG. 14B

		N,	pilot2 =	= 7		N _{pilot2} = 8								
Bit #	0	11.2 6. 2-1	3	41 (5	6	0		2	32.7	4	5	6	7.	
Slot #1	1	1 21	1	1, 0	1	1	12.1	ı	1	1	1	1	0	
2	1	1 0	1	1 31	1	1	31	1	0.	1	12	1	1	
3	1	0 0	1	0 1	1	ı	0	1	0	1	0	l	15	
4	1	n - 01	1	3251+	1	1	31	1	0	1	1	1	- 1	
5	1	1_0 1	1	1 . 0	. 1	1	1	1	1	1	1	1	0	
6	1	1 0	1	1 1	1	1	1	1	0	1	1	1	i	
7	1	i i	1	0 1	1	1	1.	1	1	1	0	1	1	
8	1	1 0	1	0, 0	1	1	2-1	1	0	1	0	1	0.	
9	1	0 0	1	0 1	1	1	0.	1	0	1	0	1	1	
10	1	0 1	1	0:4 0:	1	1	ő	1	1,	1	0	1	-0	
11	1	. 1 - 71	1	1 0	1	1	1.	1	15	1	12	1	0	
12	1	_(0,1)	1	00 0	1	1	± 0	1	1.	1	0	1	0	
13	1	0 0	1	0 1	1	1	0	1	0	1	0	1	1	
14	1	0 1	1	0 0	1	1	0.	1	15.	1	0 :	1	0	
15	1	-0 0	l	1. 0	1	1	0,	1	0	1	17	1	0	
16	1	0 1	ı	1 .	1	1	.0	l	1	1	1	1	1.	

N _{pilot}	Pilot bit position #	Corresponding word of length 16
	0	C ₁
_	1	C ₂
5	3	C ₃
	4	C.
	1	C ₁
	2	C ₂
6	4	C ₃
	5	C ₄
	1	C ₁
	2	C ₂
7	4	C ₃
	5	C ₄
	I	Cı
	3	C ₂
8 .	5	C ₃
	7	C ₄

FIG. 14C

FIG. 14 D



	R _x (0)	R _x (1)	R _x (2)	R _x (3)		R _x (5)	R _x (6)	R _x (7)	R _x (8)	R _x (9)	R _x (10)	R _x (11)	R _x (12)	R _x (13)	R _x (14)	R _x (15)
A ₁ POINT	16	4	0	4	0	-4	0	-4	-16	-4	0	-4	0	4	0	4
A ₂ POINT	16	-4	0	-4	0	4	0	4	-16	4	0	4	0	-4	0	-4
A ₃ POINT	16	4	0	4	0	-4	0	-4	-16	-4	0	-4	0	4	0	4
A₄ POINT	16	-4	0	-4	0	4	0	4	-16	4	0	4	0	-4	0	-4
B POINT	64	0	0	0 `	0	0	0	0	-64	0	0	0	0	0	0	0

FIG. 14E

	R _x (0)	R _x (1)	R _x (2)	R _x (3)	R _x (4)		R _x (6)	R _x (7)	R _x (8)	R _x (9)	R _x (10)	R _x (11)	R _x (12)	R _x (13)	R _x (14)	R _x (15)
A ₁ POINT +A ₂ POINT	32	0	0	0	0	0	0	0	-32	0	0	0	0	0	0	0
A ₃ POINT +A ₄ POINT	32	0	0	0	0	0	0	0	-32	0	0	0	0	0	0	0
A ₁ POINT +A ₄ POINT	32	0	0	0	0	0	0	0	-32	0	0	0	0	0	0	0
A ₂ POINT + A ₃ POINT	32	0	0	0	0	0	0	0	-32	0	0	0	0	0	0	0

FIG. 14F

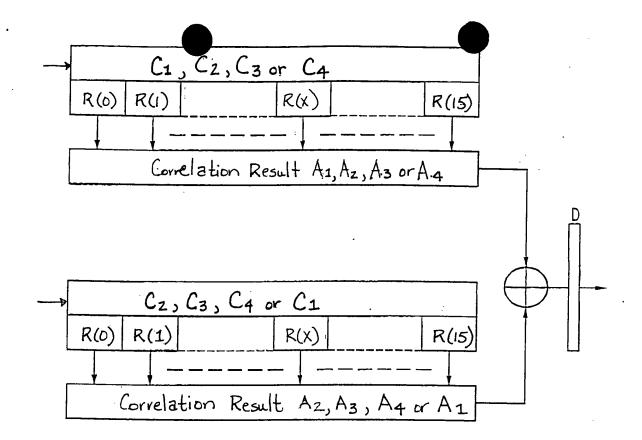


FIG. 14G

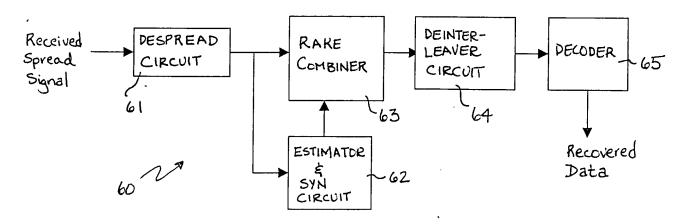


FIG. 14H

FIG. 14I

	R _x (0)	R _x (1)	R _x (2)	R _x (3)	R _x (4)	R _x (5)	R _x (6)	R _x (7)	R _x (8)	R _x (9)	R _x (10)	R _x (11)	R _x (12)	R _x (13)	R _x (14)	R _x (15)
A ₁ POINT	16	-4	-4	8	0	-4	0	0	-4	0	0	-4	0	8	-4	-4
A ₂ POINT	16	0	0	-4	-4	-4	0	0	12	0	0	-4	-4	-4	0	0
A₃ POINT	16	4	0	0	4	8	8	0	0	0	8	8	4	0	0	4
A ₄ POINT	16	0	4	-4	0	0	-4	4	0	4	-4	0	0	-4	4	0
B POINT	64	0	0	0	0	0	4	4	8	4	4	0	0	0	0	0

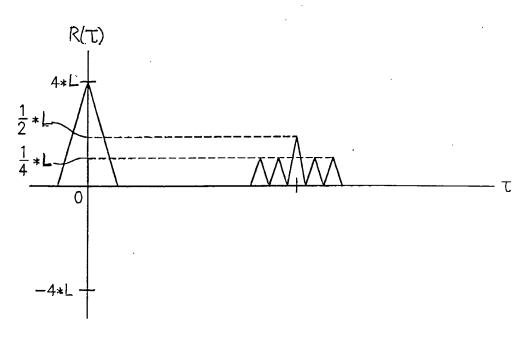


FIG. 14J

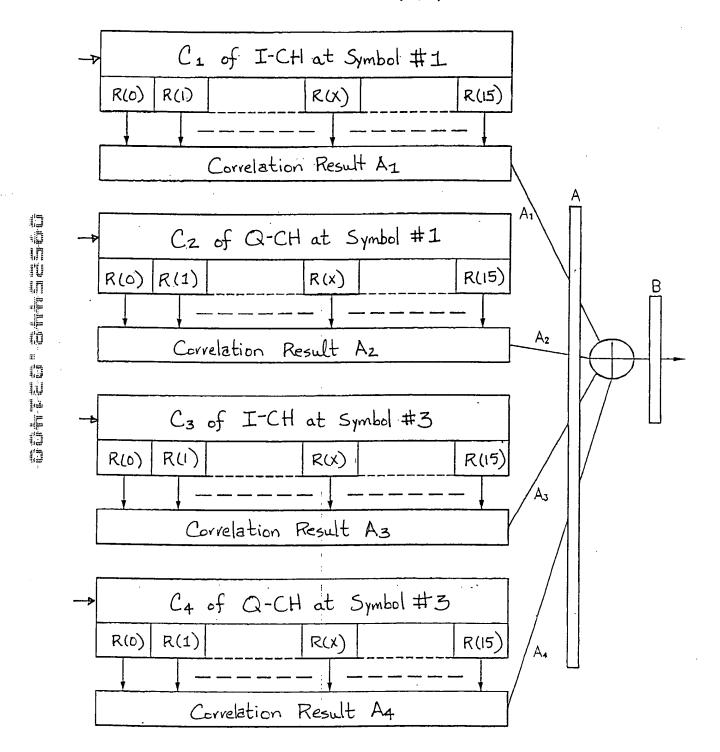
	N _{pilot} 4	$N_{pilot} = 8$	$\Lambda_{\rm ipnot} = 16$
Symbol #	0 1 0	14 14 14 14 14 14 14 14 14 14 14 14 14 1	0 1 2 3 4 5 6 7
Slot #1	11 11 1	1 11 10	11 11 10 11 11 11 01
2	11 10 1	1 10 11 11	11 10 11 11 11 01 11 11
3	11 00 1	1 00 11 01	11 00 11 01 11 11 11 01
4	11 10 1	1 10 11 11	11 10 11 11 11 10 11 00
5	11 11 1	1 11 11 10	11 11 11 10 11 00 11 01
6	11 10 1	1 10 11 11	11 10 11 11 11 01 11 00
7	11 11 1	1 11 11 01	11 11 11 01 11 00 11 10
8	11 10 1	1 10 11 00	11 10 11 00 11 01 11 11
9	11 00 1	1 00 11 01	11 00 11 01 11 00 11 10
10	11 01 1	1 01 11 00	11 01 11 00 11 10 11 00
11	11 11 11	1 11 11 10	11 11 11 10 11 00 11 10
12	11 01 11	1 .01 11 00	11 01 11 00 11 01 11 11
13	11 00 11	1 00 11 01	11 00 11 01 11 11 11 10
14	11 01 11	1 01 11 00	11 01 11 00 11 10 11 11
15	11 00 11	1 00 11 10	11 00 11 10 11 11 11 01
16	11 01 11	1 01 11 11	11 01 11 11 11 10 11 00

FIG. 15A

Symbol rate	Symbol #	Channel	Corresponding Word of length L=16
		I-CH	Cı
$N_{pilot} = 4$	1	Q-CH	C ₂
		I-CH	C ₁
	1	Q-CH	C ₂ C ₁ C ₂ C ₂ C ₃ C ₄ C ₁ C ₂ C ₃ C ₄ C ₁ C ₂ C ₃
$N_{pilot} = 8$	_	I-CH	C ₃
	3	Q-CH	C ₄
· · · · · · · · · · · · · · · · · · ·	l I-CH Q-CH	I-CH	C ₁
		Q-CH	C ₂
		I-CH	C ₃
	3	Q-CH	C4
$N_{pilot} = 16$	_	I-CH	C ₅
	5	Q-CH	C ₆
	_	I-CH	C ₇
	7	Q-CH	C ₈ .

FIG. 15B

FIG 15C



Symbol #	0	1	2	3
Slot #1	11	11	11	10
2	11	10	11	11
3	11	00	11	01
4	11	10	11	11
5	11	11	11	10
6	11	10	11	11-
7	11	11	11	01
8	11	10	11	00
9	11	00	11	01
10	11	01	11	00
11	11	11	11	10
12	11	•01	11	00
13	11	00	11	01
14	11	01	11	00
15	11	00	11	10
16	11	01	11	11

FIG. 16A

FIG. 16B

Symbol #	Channel	Corresponding word of length 16	
1	I-CH	C_1	
1	Q-CH	C ₂	
2	I-CH	C ₃	
	Q-CH	C ₄	

	P _{pilot} = 8	N _{pilot} = 16
Symbol #	0 1 2 3	0 1 2 3 4 5 6 7
Slot #1	11 11 11 10	11 11 11 10 11 11 11 01
2	11 10 11 11	11 10 11 11 11 01 11 11
3	11 00 11 01	11 00 11 01 11 11 11 01
4	11 10 11 11	11 10 11 11 10 11 00
5	11 11 11 10	11 11 11 10 11 00 11 01
6	11 10 11 11	11 10 11 11 11 01 11 00
7	11 11 11 01	11 11 11 01 11 00 11 10
8	11 10 11 00	11 10 11 00 11 01 11 11
9	11 00 11 01	11 00 11 01 11 00 11 10
10	11 01 11 00	11 01 11 00 11 10 11 00
11	11 11 11 10	11 11 11 10 11 00 11 10
12	11 01 11 00	11 01 11 00 11 01 11 11
13	11 00 11 01	11 00 11 01 11 11 11 10
14	11 01 11 00	11 01 11 00 11 10 11 11
15	11 00 11 10	11 00 11 10 11 11 11 01
16	11 01 11 11	11 01 11 11 11 10 11 00

FIG. 16C

Symbol rate	Symbol #	Channel	Corresponding word of length 16
		I-CH	Cı
	1	Q-CH	C ₂
$N_{pilot} = 8$	_	I-CH	C ₃
	3	Q-CH	C ₄
		I-CH	C ₁
	1	1 Q-CH	C ₂
		I-CH	C ₃
	3	Q-CH	C.
$N_{pilot} = 16$		I-CH	C ₅
	5	I-CH C₃ Q-CH C₄ I-CH C₅ Q-CH C₆	C ₆
	_	I-CH	C ₇
77	7	Q-CH	C ₈

FIG. 16D

FIG. 17A

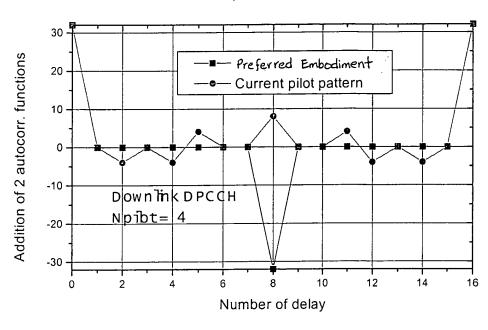
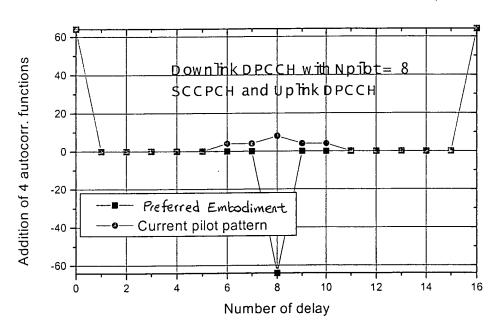
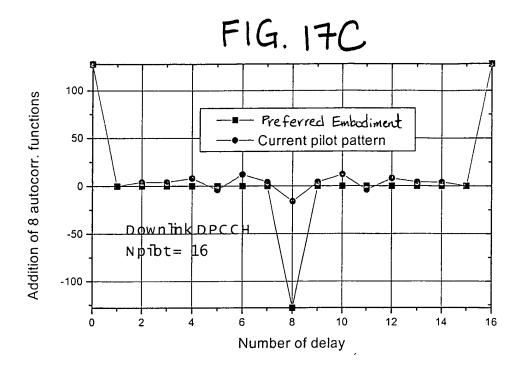


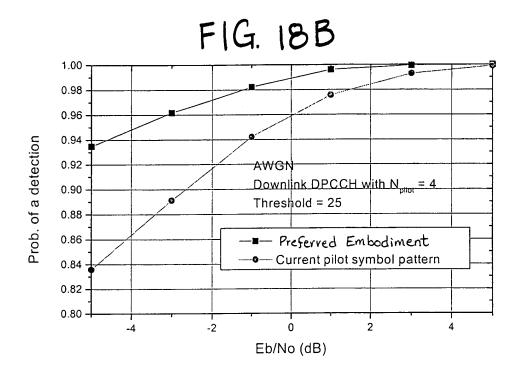
FIG 17B

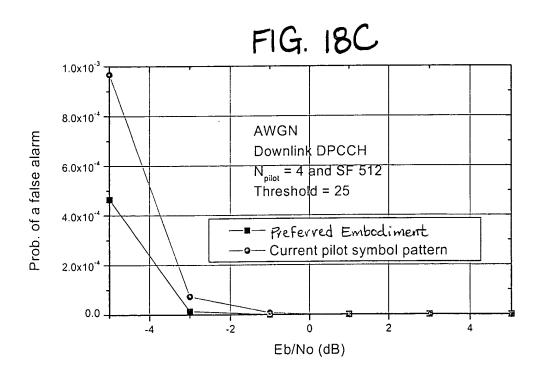


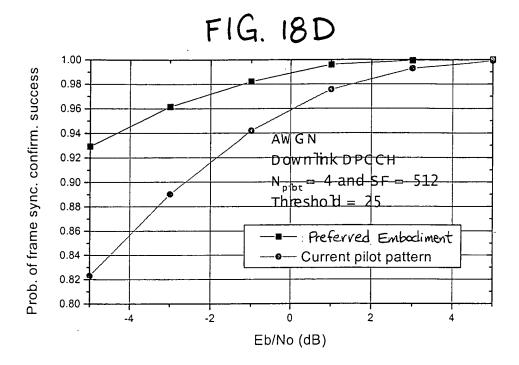


Parameters	Downlink	
Slot per frame	16	
Number of bits in the DPCCH (Pilot/TPC/TFCI)	4/2/0	
Number of bits in the DPDCH per each slot	4	
Spreding factor (DPDCH)	512	
Spreding factor (DPCCH)	512	
Modulation	QPSK	
3dB bandwidth	4.096MHz	
Shaping filter	Root raised cosine (roll off 0.22)	
Power amplifier	Ideal	
Propogation channel	AWGN	

FIG. 18A







	N _{pilot} =	$N_{pilot} = 8$	N _{pilot} 16
Symbol #	0 1	0 1 2 3.	0 1 2 3 4 5 6 7
Slot #1	01 10	11 00 00 10	11 00 00 10 11 11 00 10
2	00 10	11 01 00 11	11 01 00 11 11 01 00 00
3	10 10	11 11 00 01	11 11 00 01 11 11 00 10
4	00 10	11 01 00 11	11 01 00 11 11 10 00 11
5	01 10	11 00 00 10	11 00 00 10 11 11 00 01
6	00 10	11 01 00 11	11 01 00 11 11 10 00 00
7	01 10	11 11 00 10	11 11 00 10 11 00 00 01
8	00 10	11 10 00 11	11 10 00 11 11 01 00 00
9 .	10 10	11 11 00 01	11 11 00 01 11 00 00 01
10	11 10	11 : 10 00 00	11 10 00 00 11 10 00 11.
11	01 10	11 00 00 10	11 00 00 10 11 00 00 01
12	11 , 10	11 10 00 00-	11 10 00 00 11 01 00 00.
13	10, 10	11 11 00 01	11 11 00 01 11 00 00 10
14	11 10	11 10 00 00	11 10 00 00 11 01 00 11
15	10 10	11 00 00 01	11 00 00 01 11 11 00 10
16	11 10	11 01 00 00	11 01 00 00 11 10 00 11

FIG. 19A

Symbol rate	Symbol #	Channel	Corresponding Word of length 16
		I-CH	-C ₁
$N_{pilot} = 4$	0	Q-CH	C ₂
		I-CH	-C ₃
	1	Q-CH C ₄	C ₄
$N_{pilot} = 8$	3	I-CH	C ₁
		Q-CH	-C ₂
		I-CH	-C ₃
	1	Q-CH C ₄	C ₄
	2	I-CH	C ₁
	3	Q-CH	-C ₂
$N_{pilot} = 16$	_	I-CH	-C ₇
	5	Q-CH	C ₈
	7	I-CH	C ₅
		Q-CH	-C ₆

FIG. 19B 22/57

FIG. 19C

Symbol #	Channel	Corresponding word of length 16
	I-CH	Cı
1	Q-CH	C ₂
	I-CH	-C ₃
3	Q-CH	-C ₄

FIG. 19D

	$N_{pilot} = 8$	1 pmol = 16
Symbol #	0 1 2 3	0 1 2 3 4 5 6 7
Slot #1	11 00 00 10	11 00 00 10 11 11 00 10
2	11 01 00 11	11 01 00 11 11 01 00 00
3	11 11 00 01	11 11 00 01 11 11 00 10
4	11 01 00 11	11 01 00 11 11 10 00 11
5	11 00 00 10	11 00 00 10 11 11 00 01
6	11 01 00 11	11 01 00 11 11 10 00 00
7	11 11 00 10	11 11 00 10 11 00 00 01
8	11 10 00 11	11 10 00 11 11 01 00 00
9	11 11 00 01	11 -11 00 01 11 00 00 01
10	11 10 00 00	11 10 00 00 11 10 00 11
11	11 00 00 10	11 00 00 10 11 00 00 01
12	11 10 00 00	11 10 00 00 11 01 00 00
13	11 11 00 01	11 11 00 01 11 00 00 10
14	11 10 00 00	11 10 00 00 11 01 00 11
15	11 00 00 01	11 00 00 01 11 11 00 10
16	11 01 00 00	11 01 00 00 11 10 00 11

FIG. 19E

Symbol rate	Symbol #	Channel	Corresponding word
		I-CH	-C ₃
	1	Q-CH	C ₄
$N_{pilot} = 8$		I-CH	C ₁
	3	Q-CH	-C ₂
		I-CH	-C ₃
	1	Q-CH C ₄	C ₄
		I-CH	C ₁
	3	Q-CH	-C ₂
$N_{pilot} = 16$	_	I-CH	-C ₇
	5	Q-CH	C ₈
	7	I-CH	C₅
		Q-CH	-C ₆

FIG. 19F

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Sequence	Autocorrelation
$C_1 = (1\ 1\ 0\ 1\ 1\ 1\ 1\ 1\ 0\ 0\ 1\ 0\ 0\ 0\ 0)$	16 4 0 4 0 -4 0 -4 -16 -4 0 -4 0 4 0 4
$C_2 = (1\ 0\ 0\ 0\ 1\ 0\ 1\ 0\ 0\ 1\ 1\ 1\ 0\ 1\ 0\ 1)$	16 -4 0 -4 0 4 0 4 -16 4 0 4 0 -4 0 -4
$C_3 = (1 \ 1 \ 1 \ 1 \ 1 \ 0 \ 1 \ 1 \ 0 \ 0 \ $	16 4 0 4 0 -4 0 -4 -16 -4 0 -4 0 4 0 4
$C_4 = (0\ 1\ 0\ 1\ 0\ 0\ 0\ 1\ 1\ 0\ 1\ 0\ 1\ 1\ 1\ 0)$	16 -4 0 -4 0 4 0 4 -16 4 0 4 0 -4 0 -4
$C_5 = (0\ 0\ 1\ 1\ 1\ 0\ 1\ 1\ 1\ 1\ 0\ 0\ 0\ 1\ 0\ 0)$	16 4 0 -4 0 4 0 -4 -16 -4 0 4 0 -4 0 4
$C_6 = (0\ 0\ 1\ 0\ 0\ 1\ 0\ 1\ 1\ 1\ 0\ 1\ 0\ 1\ 0)$	16 -4 0 4 0 -4 0 4 -16 4 0 -4 0 4 0 -4
$C_7 = (0\ 1\ 1\ 1\ 0\ 0\ 0\ 0\ 1\ 0\ 0\ 0\ 1\ 1\ 1\ 1)$	16 4 0 -4 0 4 0 -4 -16 -4 0 4 0 -4 0 4
$C_8 = (1\ 0\ 1\ 1\ 1\ 0\ 1\ 0\ 0\ 1\ 0\ 0\ 1\ 0\ 1)$	16 -4 0 4 0 -4 0 4 -16 4 0 -4 0 4 0 -4
$C_9 = (0\ 0\ 1\ 1\ 0\ 1\ 1\ 1\ 1\ 1\ 0\ 0\ 1\ 0\ 0)$	16 4 0 4 0 -4 0 -4 -16 -4 0 -4 0 4 0 4
$C_{10} = (0\ 0\ 1\ 0\ 1\ 0\ 0\ 1\ 1\ 1\ 0\ 1\ 0\ 1\ 1\ 0)$	16 -4 0 -4 0 4 0 4 -16 4 0 4 0 -4 0 -4
$C_{11} = (1\ 1\ 0\ 0\ 0\ 0\ 0\ 1\ 0\ 1\ 1\ 1\ 1\ 1\ 0)$	16 4 0 4 0 -4 0 -4 -16 -4 0 -4 0 4 0 4
C_{12} = (1 0 1 1 1 0 0 1 0 1 0 0 0 1 1 0)	16 -4 0 -4 0 4 0 4 -16 4 0 4 0 -4 0 -4
$C_{13} = (0\ 1\ 0\ 0\ 0\ 0\ 1\ 1\ 1\ 0\ 1\ 1\ 1\ 0\ 0)$	16 4 0 -4 0 4 0 -4 -16 -4 0 4 0 -4 0 4
$C_{14} = (1\ 0\ 0\ 0\ 1\ 0\ 0\ 1\ 0\ 1\ 1\ 1\ 0\ 1\ 1\ 0)$	16 -4 0 4 0 -4 0 4 -16 4 0 -4 0 4 0 -4
$C_{15} = (0\ 0\ 0\ 0\ 1\ 0\ 0\ 0\ 1\ 1\ 1\ 1\ 0\ 1\ 1\ 1)$	16 4 0 -4 0 4 0 -4 -16 -4 0 4 0 -4 0 4
C_{16} =(1 0 0 1 0 0 0 1 0 1 1 0 1 1 1 0)	16 -4 0 4 0 -4 0 4 -16 4 0 -4 0 4 0 -4

FIG. 20A

R(τ) τ	0	1	2	3	4	5	6	7	8	9	10	-11	12	13	14	15
$R_{E}(\tau)$.	16	4	0	4	0	-4	0	-4	-16	-4	0	-4	0	4	0	4
$R_F(\tau)$	16	-4	0	-4	0	4	0	4	-16	4	0	4	0	-4	0	-4
$R_G(\tau)$	16	4	0	-4	0	4	0	-4	-16	-4	0	4	0	-4	0	4
R _H (τ)	16	-4	0	4	0	-4	0	4	-16	4	0	-4	0	4	0	-4

FIG. 20B

<u></u>	_,				,—					_		
		N _{pilot}	= 6					Npilote	= 8			
Bit#	0	1 2	3	41.2.5	0		2	3.3	4	5,5	6	77.
Slot #1	1	1 · · · · · · · · · · ·	1	1 0	1	: ,1	i	1	1	1	1	0.
2	1	1. 0	1		1		1	. 0	1	1	1	
3	1	0 0	ı	1 + + 0	1	0	1	0.	l	1,	1	0 .
4	1	1 0	1	.121	1	1	1	0	1	1, 1	1	1
5	1	i si	1	10	ì	1	1	1.1.1 1.6.24	l		1	0
6	1	1 0	1	0 0	1	1	l	0	1	0	1	0
7	1	1, 1	1	1 0	1	1	1	5	1	1,	1	0
8	1	1 0	1	1	1	1	l	0.	1		1	
9	l	0 0	. 1	0 1 .	1	0	1	0.	1	0	1	
10	1	0. 1	1	0 - 0	1	0	1		1	2 0 1:	1	0
11	1	1 . 1	1	0 1	1		1	.1.	1	0	1	\$\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
12	1	0 .1	1	0 0	1	*.0::	1		1	0	1	.0
13	1	0 . 0	1	0 1	1	0	1	0.	1	0	1	
14	1	0 1	1	1 1	1	0	1	1.	1		1	
15	1	0 0	1	0 : 11	1	0 ;	1	0	I	0 3	1	,1
16	1	0	l	0 0	l	0	l	~ 1	1	0	1	0.

FIG. 20C

N _{pilots}	Pilot bit position #	Corresponding word of length 16				
	1	C ₁				
	2	C ₂				
6	4	C ₃				
	5	C ₄				
	1	Ct				
	3	C ₂				
8	5	C ₃				
	7	C ₄				

FIG. 20D

Symbol rate	8ksps	16,32,64,128ksps	256,512,1024ksps
Symbol #	0 1	0 1 2 3	0 1 2 3 4 5 6 7
Slot # 1	11 11	11 11 11 10	11 11 11 10 11 00 11 01
2	11 10	11 10 11 11	11 10 11 11 11 00 11 10
3	11 00	11 00 11 10	11 00 11 10 11 11 11 11
4	11 10	11 10 11 11	11 10 11 11 11 10 11 11
5	11 11	11 11 11 10	11 11 11 10 11 10 11 01
6	11 . 10	11 10 11 00	11 -10 11 00 11 01 11 00
7	11 - 11:	11 11 11 10	11 41 11 10 11 10 11 01
8	11 10	11 10 11 11	11 10 11 11 11 11 11 00
9	11 00	11 00 11 01	11 00 11 01 11 11 11 11
10	11 01	11 01 11 00	11 01 11 00 11 11 11 01
11	11 115	11 11 11 01	11 11 11 01 11 00 11 00
12	11 01	11 01 11 00	11 01 11 00 11 01 11 00
13	11 00	11 00 11 01	11 00 11 01 11 01 11 10
14	11 01	11 01 11 11	11 .01 11 11 10 11 11
15	11 00	11 00 11 01	11 00 11 01 11 01 11 10
16	11 01	11 01 11 00	11 01 11 00 11 00 11 11

FIG. 20E

Symbol rate								2048,409	96ks _l	os						_
Symbol #	0	1	2	3	4	5	6	7.	8	9	10	11	12	13	14	15
Slot # 1	11	. 11	11	10	11	00	11	01	11	00	11	11,5	11	_0i	11	. 01
2	11	10	11	-11	11	00	11	10	11	00	11	10	11	10	11	00
3	11	00	. 11	10	11	11	11	11	11	11	11	01	11	00	11	00
4	11	10	11	11	11	10	11	11	11	10	11	01	11	00	11	01
5	11	11	. 11	10	11	10	11	01	11	01	11	01 ~	11	01	11	10
6	11	10	11	00	11	01	li	00	11	10	11	00	11	00	11	.00
7	11	11.	11	- 10	11	10	11	01	11	10	11	. 00	11	10	11	00
8	11	10	11	11	11	11	11	00	11	- 11	11	11	11	11	11	'01
9	11	00	11	01	11	.11	11	10	11	11	11	00.	11	10	11	- 10
10	11	01	11	.00	11	11	11	01	11	1,1	11	01 =	11	01	11	1.1
11	11	11	11	01	11	00	11	00.	11	00	11	10	11	11	11	1.1
12	11	01 . !	11	00	11	01	11	.00	11	01	11	10	11	11	ì 1	10
13	11	00	11	01	11	01	11	10	11	10	11	10	11	10	11	. 01
14	11	01	11	11	11	10	11	11	11	01	11	11	11	11	11	11
15	11	00	11	01	11	01	11	10	11	01	11	11	11	. 01	11	11
16	11	01	11	00	11	00	11	11.	11	00	11	00	11	00	11	10

FIG 20F

Symbol rate	Symbol #	Channel	Corresponding word
		I-CH	C ₁
8ksps	1	Q-CH	C ₂
		I-CH	C ₁
	1		C_2
16, 32, 64, 128ksps		I-CH	C ₃
	3	I-CH Q-CH Q-CH	C ₄
	op	I-CH	C_1
	1	Q-CH	C ₂
	2	I-CH	C ₃
256 512 122 1	3	Q-CH	C ₄
256, 512, 1024ksps	-	I-CH	C ₅
	5	Q-CH	C ₆
	7	I-CH	C ₇
	7	Q-CH	C ₈
	1	I-CH	C ₁
	1	Q-CH	C ₂
	_	I-CH	C ₃
	3	Q-CH	C ₄
	_	I-CH	C ₅
	5	Q-CH	C ₆
	_	I-CH	C ₇
2049 4000	7	Q-CH	C ₈
2048, 4096ksps	0	I-CH	C ₉
	9	Q-CH	C ₁₀
		I-CH	C ₁₁
	11	Q-CH	C ₁₂
	12	I-CH	C ₁₃
:	13	Q-CH	C ₁₄
	1.5	I-CH	C ₁₅
	15	Q-CH	C ₁₆

FIG. 20G

Symbol #	0	1 - 1	2	3
Slot #1	11	11.	11	. 10
2	11	10	11	ារ
3	11	00	11	10
4	11	10	11	. 11
5	11	11	11	10
6	11	10	11	00
7	11	11	11	10
8	11	5.10	11	îi
9	11	000	11	01
10	11	01	11	00
	11	11	11	0 1.
12	11	01	11	00
13	11	00	11	01
14	11	01	11	11
15	11	00	11	01
16	11	01	11	00

FIG. 20H

Symbol #	Channel	Corresponding word of length 16
1	I-CH	C ₁
I	Q-CH	C_2
2	I-CH	C ₃
3	Q-CH	C ₄

FIG. 20I

 $R_{1,2}(\tau)+R_{2,1}(\tau+1)$

	Frame Synchronization Words									
L=15, Slot No.	1 2 3 415									
	$C_i = (1\ 0\ 0\ 0\ 1\ 1\ 1\ 1\ 0\ 1\ 0\ 1\ 1\ 0\ 0)$									
	$C_2 = (1\ 0\ 1\ 0\ 0\ 1\ 1\ 0\ 1\ 1\ 0\ 0\ 0)$									
	$C_3 = (1\ 1\ 0\ 0\ 0\ 1\ 0\ 0\ 1\ 1\ 0\ 1\ 0\ 1\ 1)$									
	$C_4 = (0\ 0\ 1\ 0\ 1\ 0\ 0\ 0\ 1\ 1\ 1\ 0\ 1\ 1)$									
	$C_5 = (1 \ 1 \ 1 \ 0 \ 1 \ 0 \ 1 \ 0 \ 0 \ 0 \ $									
	$C_6 = (1\ 1\ 0\ 1\ 1\ 1\ 0\ 0\ 0\ 0\ 1\ 0\ 1\ 0\ 0)$									
	$C_7 = (1\ 0\ 0\ 1\ 1\ 0\ 1\ 0\ 1\ 1\ 1\ 0\ 0\ 0)$									
	$C_8 = (0\ 0\ 0\ 0\ 1\ 1\ 1\ 0\ 1\ 1\ 0\ 1\ 0\ 1)$									

FIG. 21

FIG. 22A

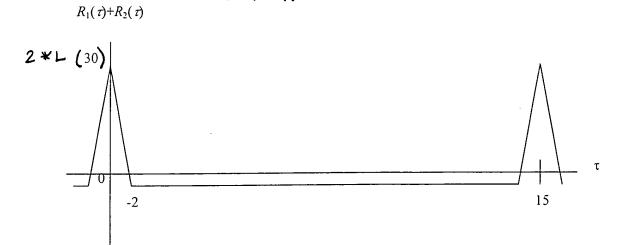
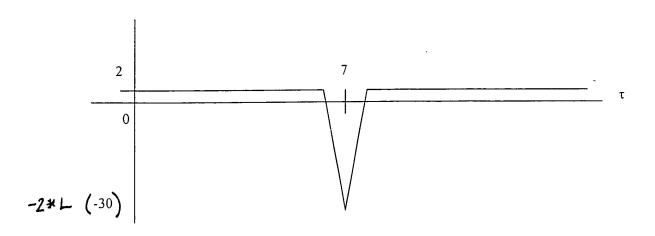


FIG. 22B



 $R_1(\tau) + R_2(\tau) + R_3(\tau) + R_4(\tau)$

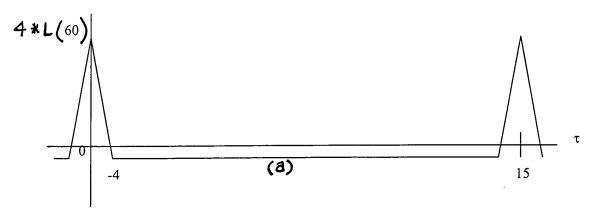


FIG. 22C

 $R_{1,2}(\tau)+R_{2,1}(\tau+1)+R_{3,4}(\tau)+R_{4,3}(\tau+1)$

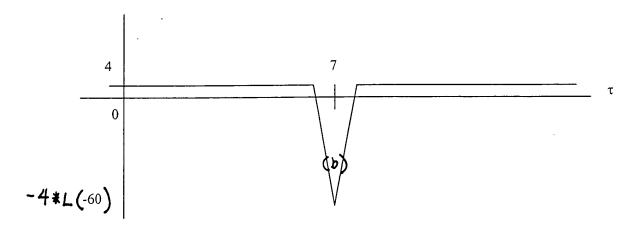


FIG. 22D

	N _{pilot} =2	N_{pilot}	=3		N _{pilo}	=4	
Bit #	0 1	0 1	2	0	12.7	_2	3.3
Slot #1	1 1 3	1 1	1	1	1.	1	1
2	0 0	0 1	0	1	0	1	0
3	0 1	0 1	1	1	0	1	1
4	0 0	0 1	0	1	0	1	0
5	1 0	1 1	.0	1	1	1	0
6	1 1 1	1 1	1.	1	1	1	1.
7	1 1 1 1	1 1	1	1	1.	1	1 1
8	1 0	1. 1	0	1	1.	1	- 0
9	0 1	0 1	1 1	1	0.	1	[4]
10	1 1 1	1, 1	1	1	1,3	1	11 1
11	0 1	0 1	1	1	0 -	1	1.1
12	1 1 0.	1 1	0	1	\cdot \cdot 1°	1	, 0.
13	1 0	1 1	0	1	1.	1	0
14	0 0	0 1	0.	1	0	1	0.
15	0 0	0 1	0.	1	0	1_	· * 0

FIG. 23A

N _{pilot}	Pilot bit position #	Corresponding word of length 15				
	0	C_1				
2	1	C_2				
2	0	C ₁				
3	2	C_2				
	1	C ₁				
4	3	C_2				

FIG. 23B

FIG. 23C

	N _{pil}	ot=2	J	V _{pilot} =	3		N_{pile}	ot=4	
Bit #	0	1	0	1	2.	0	1	2	3
Slot #1	1	1	:1	1	1	1	1	1	1
2	1	0	0	1		1	0	1	0
3	1	1.	0	1	1	1	70	1	1 1
4	1	∙.0 ∷	``0 ∵	1	0	1	0	1	0
5	1	0	1.	1	0	1	1.1	1	0
6	1	i	1	1	,1 -	1	1	1	1
7	1	1	1.	1	1 1	1	1	1	1
8	1	* 0 :	1.	1	0	1	1	1	0
9	1	<i>"</i> 1₀∵	.0	1	1 1 :	1	0	1	13
10	1	1	1.	1	$ \cdot $	1	1 .	1	1
11	1	, 1	0	1	1	1	0	1	1 1
12	1	0	15	1	0	1	1	1	0
13	1	0	1.	1	0	1	[1]	1	0
14	1	0	0	1	0	1	0	1	0
15	1	0	0	1	0	1	0	1	0

N_{pilot}	Pilot bit position #	Corresponding word of length 15
2	1	C_1
2	0	Cı
3	2	C ₂
4	1	Cı
4	3	C_2

FIG. 23D

FIG. 23E

	N _i	oilot =	= 5	$N_{pilot} = 6$					
Bit#	0 1	2	3 4	0	1 2	3	4 5		
Slot #1	1 1	1	1 0	1	1 1	1	1 0:		
2	0 0	1	1 0	1	0 0	1	1 0		
3	0 1	1	0 1	1	0 1	1	0 1		
4	0 0	1	0 0	1	0 0	1	0.4 0 .		
5	1 0	1	0 1	1	1 0	1	0, 1		
6	1 1	1	1 0	1	1 1	1	1 0		
7	1 1	1	0 0.	1	1	1	0 : 10		
8	1 0	1	0 0	1	1 0	1	0 50		
9	0 1	1.	1 . 0	1	0 1	1	1 0		
10	1 1	1	1	1	1 1	1	12.7		
11	0 1	1	0 1	1	0 1	1	0: 1		
12	1 0	1	1. 1.	1	1 0	1	1,700		
13	1 0	1	0 0.	1	1 0	1	0. 0		
14	0 -0	1	1; 1	1	0 0	1	1 1		
15	0 0	1	1 1	1	0 0	1	1		

		N _I	oilot =	= 7					N_{pilot}	= 8			
Bit #	0	1 2	3	4 5	6	0	1	2	3	4	5	6	7
Slot #1	1	1 1	1	1 . 0	1	1	1	1	1	1	1.	1	0
2	1	0 0	1	1 0	1	1	0	1	0	1	1	1	0
3	1	0 1	1	0 1	1	1	. 0	1	- 1	1	0	1	1
4	1	0 0	1	0 0	1	1	0	1	0	1	0	1	0
5	1	1 0.	1	0 1	1	1	1	1	0	1	0	1	1
6	1	1 4 1	1	1 0	1	1	1	1	1	1	1,	1	0
7	1	1 1	1	0 0	1	1	1	1	1	1	0	1	0
8	1	1 0	1	0 0	1	1	1	1	0	1	0	1	0
9	1	0 1	1	1 0	1	1	0	1	1.	1	1	1	0
10	1	1 1	1	1 1	1	1	1	1	1	1	1	1	1
11	1	0 1	1	0 1	1	1	0	1	. 1	1	0	1	1
12	1	1 0	1	1 1	1	1	1 .	1	0 .	1	1	1	1
13	1	1 0	1	0 0	1	1	1	1	0	1	0	1	0
14	1	0 0	1	1 1	1	1	0	1	0	1	1	1	1
15	1	0 0	1	1 1	1	1	0	1	0	1	1.	1	1

FIG. 23F

N_{pilot}	Pilot bit position #	Corresponding word of length 15			
A THE PROPERTY OF THE PROPERTY	0	C ₁			
-	1	C ₂			
5	3	C ₃			
	4	C ₄			
entre compression	1	Cı			
	2	C ₂			
6	4	C ₃			
	5	C ₄			
	1	C_1			
a	2	C ₂			
7	4	C ₃			
	5	C ₄			
	1	C_1			
0	3	C ₂			
8	5	C ₃			
	7	C ₄			

FIG. 23G

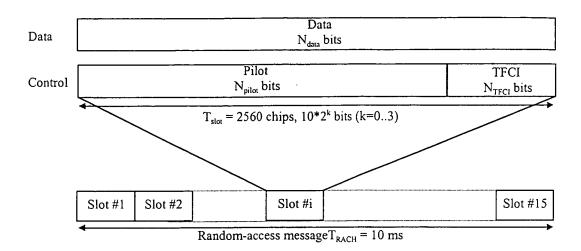


FIG. 23H

FIG. 231

	Channel Bit Rate (kbps)	Channel Symbol Rate (ksps)	SF	Bits/ Frame	Bits/ Slot	N _{pilot}	N _{TFCI}
ľ	15	15	256	150	10	8	2

FIG. 23J

Bit #	0	1.	2	3	4	'5 ∷	6	7
Slot #1	1	1	1	1	1	.1	1	0 :
2	1	0.	1	0	1	1 .	1	0 (
3	1	0	1	1	1	0	1	1 .
4	1	0	1	0	1	0	1	0
5	1	12.5	1	0	1	- 0	1	1 🧃
6	1	1	1	1	1	1.	1	0 .
7	1	1	1	1 .	1	0	i	0
8	1	1	1	0. •	1	0	1	.0
9	1	0	1	1	1	1	1	0
10	1	1	1	1	1	1	1	1
11	1	0	1	1	1	0	1	1
12	1	1	1	0	1	1	1	1
13	1	1	1	.0	1	0.	1	0
14	1	0	1	0	1	1 .	1	1
15	1	0	1	0	1	1	1	1 .

	N _{pilot} =2	N_{pil}	$N_{\text{pilot}} = 4$ $N_{\text{pilot}} = 8$ $N_{\text{pilot}} = 8$					lot = 16							
Symbol #	0	0	1	0	1	2	3_	0	1	2	.3	4	.5 :	6	7
Slot #1	11	11	: 11	11	11	11	10	11	11	11	10	11	11	11	10
2	00	11	00	11	00	11	10	11	00	11	10	11	11	11	00
3	01	11	01	11	01	11	0,1	11	01	11	01	11	10	11	00
4	00	11	00	11	00	11	00	11	00	11	00	11	01	11	10
5	10	11	10	11	10	11	01	11	10	11	01	11	11	11	11
6	11	11	11	11	- 11	11	10	11	11	11	10	11	01	11	01
7	11	11	11	11	11	11	00	11	11.	11	00	11	10	11	11
8	10	11	10	11	10	11	00	11	10	11	00	11	10	11	00
9	01	11	01	11	01 -	11	10	11	01	11	10	11	00	11	11
10	11	11	11	11	11	11	11	11	11	11	11	11	00	11	11
11	01	11	01	11	01	11	01	11	01	11	01	11	11	11	10
12	10	11	10	11	10	11	11	11	10	11	11	11	00	11	10
13	10	11	10	11	10	11	00	11	10	11	0.0	11	01	11	01
14	00	11	00	11	00	11	,11	11	00	11	11 -	11	00	11	: 00
15	00	11	00	11	00 ₫	11	1.1	11	00	11	11 j	11	10	11	01

FIG. 24A

Symbol rate	Symbol #	Channel	Corresponding word of length 15
) I		I-CH	C ₁
$N_{pilot} = 2$	0	Q-CH	C ₂
N - 4	1	I-CH	C ₁
$N_{pilot} = 4$	1	Q-CH	C ₂
	1	I-CH	C ₁
Nt _ 0	1	Q-CH	C_2
$N_{pilot} = 8$	3	I-CH	C ₃
	3	Q-CH	C ₄
	1	I-CH	C ₁
	1	Q-CH	C ₂
	3	I-CH	C ₃
N - 16	3	Q-CH '	C ₄
$N_{pilot} = 16$	5	I-CH	C ₅
	J	Q-CH	C ₆
	7	I-CH	C ₇
	/	Q-CH	C ₈

FIG. 24B

	$N_{pilot} = 4$	$N_{pilot} = 8$	$N_{pilot} = 16$
Symbol #	0 1	0 1 2 3	0 1 2 3 4 5 6 7
Slot #1	01 : 10	11 00 00 10	11 00 00 10 11 00 00 10
2	10 10	11 00 00 01	11 00 00 01 11 10 00 10
3	11 10	11 11 00 00	11 11 00 00 11 10 00 11
4	10 10	11 10 00 01	11 10 00 01 11 00 00 00
5	00 10	11 11 00 11	11 11 00 11 11 01 00 10
6	01 10	11 00 00 10	11 00 00 10 11 11 00 00
7	01 10	11 10 00 10	11 40 00 10 11 01 00 11
8	00 10	11 10 00 11	11 10 00 11 11 10 00 11
9	11: 10	11 00 00 00	11 00 00 00 11 01 00 01
10	01 10	11 01 00 10	11 01 00 10 11 01 00 01
11	11 10	11 11 00 00.	11 11 00 00 11 00 00 10
12	00 10	11 01 00 11	11 01 00 11 11 00 00 01
13	00 10	11 10 00 11	11 10 00 11 11 11 00 00
14	10 10	11 01 00 01	11 01 00 01 11 10 00 01
15	10 10	11 01 00 01	11 301 00 01 11 11 00 11

FIG. 24C

Symbol rate	Symbol #	Channel	Corresponding word of length 15
		I-CH	-C ₁
$N_{pilot} = 4$	0	Q-CH	C ₂
	1	I-CH	-C ₃
	1	Q-CH	C ₄
$N_{pilot} = 8$	2	I-CH	C ₁
	. 3	Q-CH	-C ₂
	1	I-CH	-C ₃
	1	Q-CH	C ₄
	2	I-CH	C ₁
	3	Q-CH	-C ₂
$N_{pilot} = 16$	-	I-CH	-C ₇
	5	Q-CH	C ₈
	7	I-CH	C ₅
	7	Q-CH	-C ₆

FIG. 24D

	$N_{pilot} = 8$	$N_{pilot} = 16$
Symbol #	0 1 2 3	0 1 2 3 4 5 6 7
Slot #1	11 11 11 10	11 11 11 10 11 11 11 10
2	11 00 11 10	11 00 11 10 11 11 11 00
3	11 01 11 01	11 01 11 01 11 10 11 00
4	11 00 11 00	11 00 11 00 11 01 11 10
5	11 10 11 01	11 10 11 01 11 11 11 11
6	11 11 11 10	11 11 11 10 11 01 11 01
7	11 11 11 00	11 11 11 00 11 10 11 11
8	11 10 11 00	11 10 11 00 11 10 11 00
9	11 01 11 10-,	11 01 11 10 11 000 11 11
10	11 11 11 11	11 11 11 11 11 000 11 11
11	11 01 11 01	11 01 11 01 11 11 11 10
12	11 10 11 11	11 10 11 11 11 00 11 10
13	11 10 11 00	11 10 11 00 11 01 11 01
14	11 00 11 11	11 00 11 11 11 00 11 00
15	11 00 11 11	11 00 11 11 11 10 11 01

FIG. 25A

Symbol rate	Symbol #	Channel	Corresponding word of length 15
	1	I-CH	C ₁
	1	Q-CH	C ₂
$N_{pilot} = 8$	2	I-CH	C ₃
	3	Q-CH	C ₄
	,	I-CH	Cı
	1	Q-CH	C ₂
	2	I-CH	C ₃
	3	Q-CH	C ₄
$N_{pilot} = 16$		I-CH	C ₅
	5	Q-CH	C ₆
	7	I-CH	C ₇
	7	Q-CH	C ₈

FIG 25B

	$N_{pilot} = 8$	$N_{pilot} = 16$
Symbol #	0 1 2 3	0 1 2 3 4 5 6 7,
Slot #1	11 00 00 10	11 00 00 10 11 00 00 10
2	11 00 00 01	11 00 00 01 11 10 00 10
3	11 11 00 00	11 11 00 00 11 10 00 11
4	11 10 00 01	11 10 00 01 11 00 00 00
5	11 11 00 11	11 11 00 11 11 01 00 10
6	11 00 00 10	11 00 00 10 11 11 00 00
7	11 10 00 10	11 10 00 10 11 01 00 11
8	11 10 00 11	11 10 00 11 11 10 00 11
9	11 00 00 00	11 00 00 00 11 01 00 01;
10	11 01 00 10	11 01 00 10 11 01 00 01
11	11 11 00 00	11 11 00 00 11 00 00 10
12	11 01 00 .11	11 01 00 11 11 00 00 01
13	11 10 00 11	11 10 00 11 11 11 00 00
14	11 01 00 01	11 01 00 01 11 10 00 01
15	11 01 00 01	11 01 00 01 11 11 00 11

FIG. 25C

Symbol rate	Symbol #	Channel	Corresponding word of length 15
		I-CH	-C ₃
	1	Q-CH	C ₄
$N_{pilot} = 8$	2	I-CH	C ₁
	3	Q-CH	-C ₂
	1	I-CH	-C ₃
	1	Q-CH	C ₄
	3	I-CH	C ₁
N. 16	3	Q-CH	-C ₂
$N_{pilot} = 16$	5	I-CH	-C ₇
	3	Q-CH	C ₈
	7	I-CH	C ₅
	/	Q-CH	-C ₆

FIG. 25D

Parameters	Uplink
Number of slots per frame	15
Number of bits in the DPCCH (Pilot/TPC/TFCI/FBI)	. 6/2/2/0
Number of bits in the DPDCH per each slot	10
Spreading factor (DPDCH)	256
Spreading factor (DPCCH)	256
Modulation	HPSK
3dB bandwidth	3.84MHz
Shaping filter	Root raised cosine (roll off 0.22)
Power amplifier	Ideal
Propagation channel	AWGN

FIG. 26A

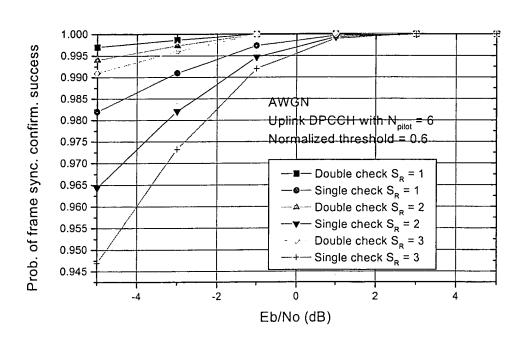


FIG. 26B

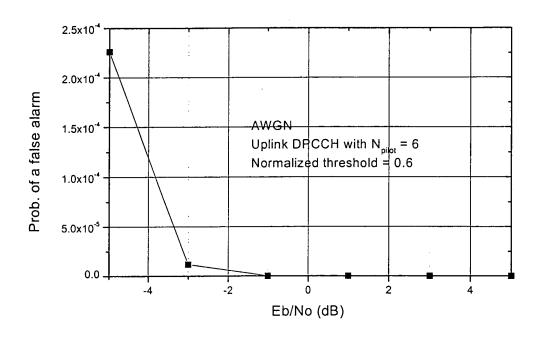
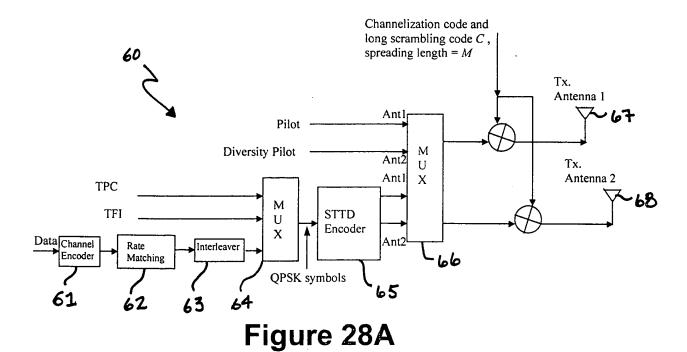


FIG. 26C

F1G. 27

Item	15 slots	16 slots
No. of slots per frame	15	16
No. of N _{pilot} per slot	1) Uplink	1) Uplink
	2, 3, 4, 5, 6, 7, 8	5, 6, 7, 8
	2) Downlink	2) Downlink
	2, 4, 8, 16	4, 8, 16, 32
Slot-Slot possible?	Yes	Yes
Double-check possible?	Yes	Yes
	(Two correltors such as auto-correlator (Auto-correlator)	(Auto-correlator)
	and cross-correlator are used)	
Single frame synchronization word can be used for frame synchronization?	Yes since a frame synchronization word has -1 out-of-phase coefficients.	May not be feasible because of +4 or -4 out-of-phase coefficients. The +4 or -4 side lobes can be zero through some particular processing using preferred pair of frame synchronization words.
Frame syncrhonzation words	All 8 frame synchronization words are made out of a single PN code	All 8 frame synchronization words have +4 or -4 out-of-phase coefficient and minus peak value at middle shift.
Autocorrelation function	$R(\tau)=15, \tau=0$	$R(\tau)=16, \tau=0$
	$R(\tau)=-1$, elsewhere	$R(\tau)=-16, \tau=8$
		$R(\tau)=0,+4$, or -4 , elsewhere
	т.	



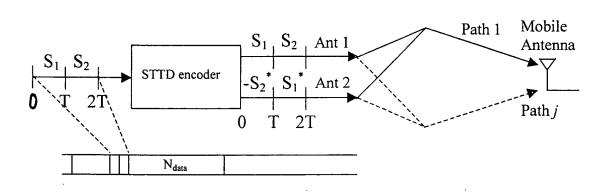


Figure 28B

FIG. 29A

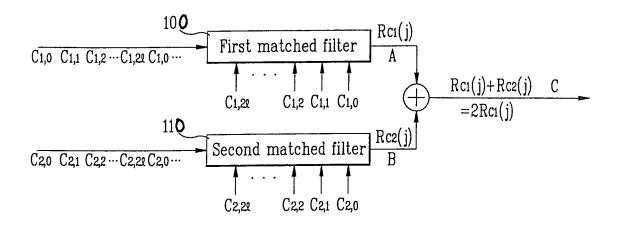


FIG. 29B

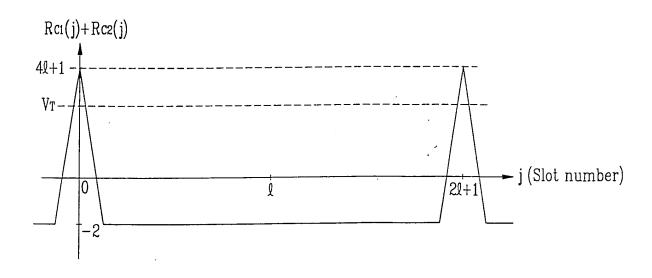


FIG. 30A

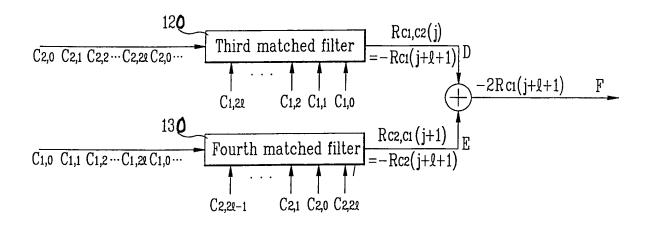


FIG. 30B

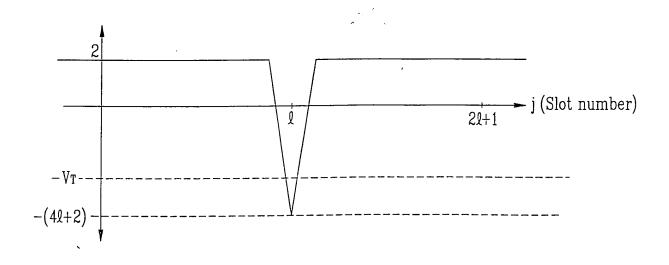


FIG. 31

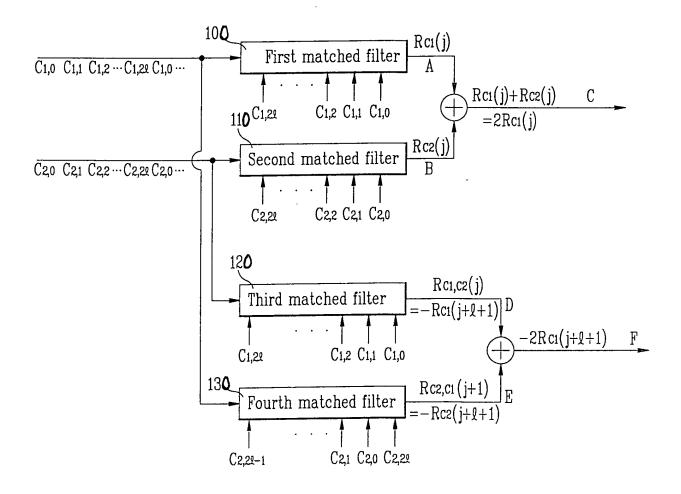


FIG. 32A

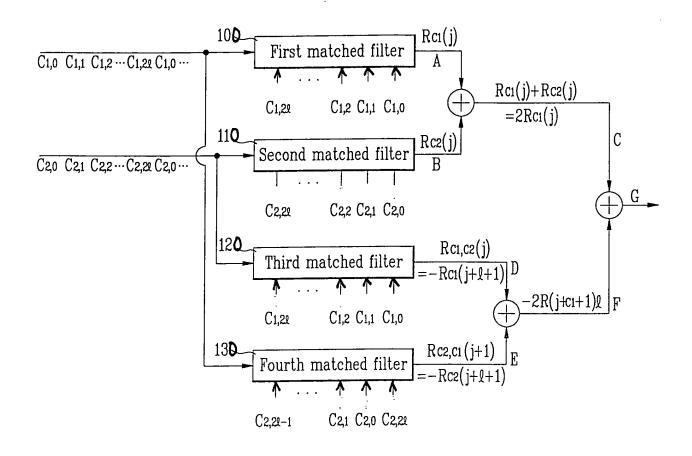


FIG. 32B

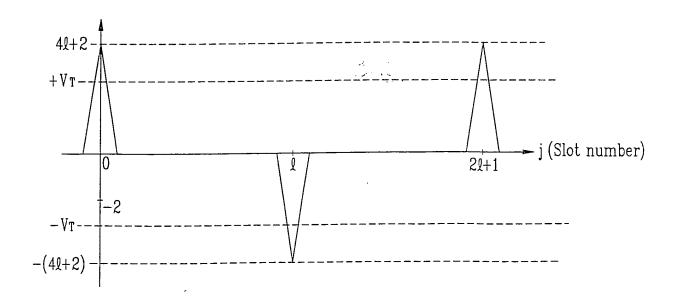


FIG. 33A

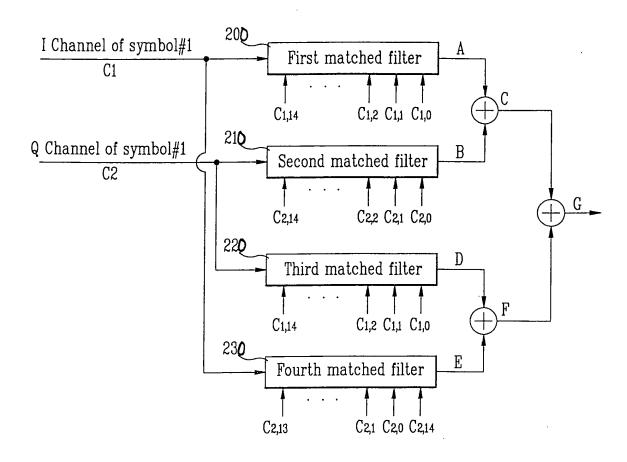


FIG. 33B

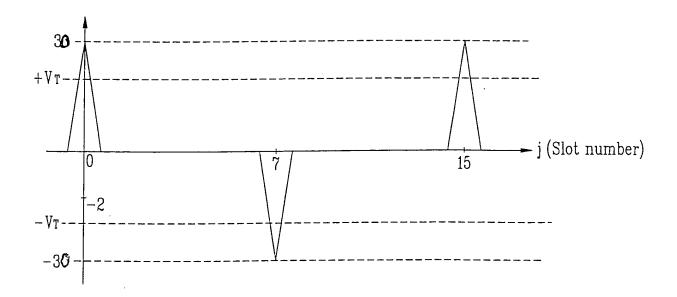


FIG. 34

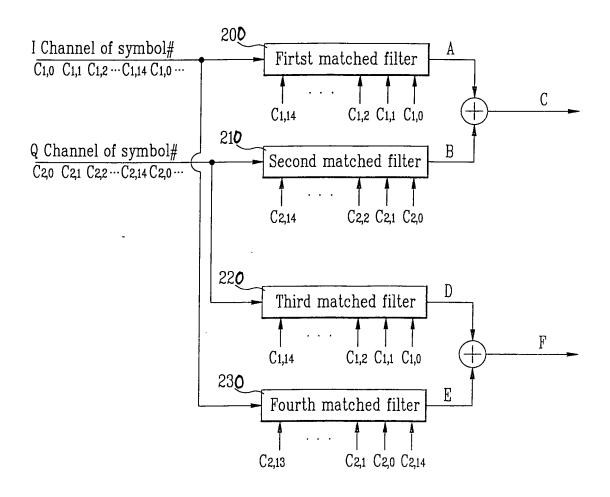


FIG. 35A

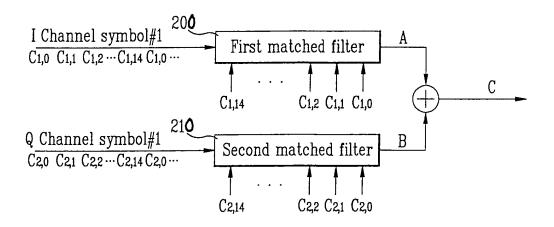


FIG. 35B

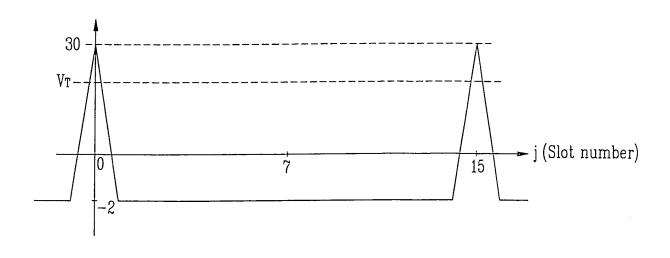


FIG. 36A

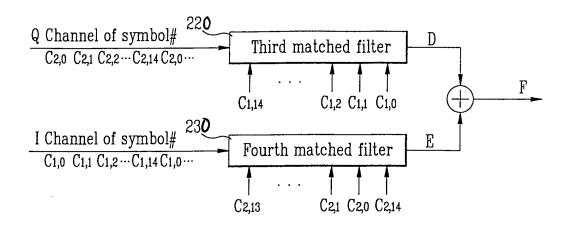


FIG. 36B

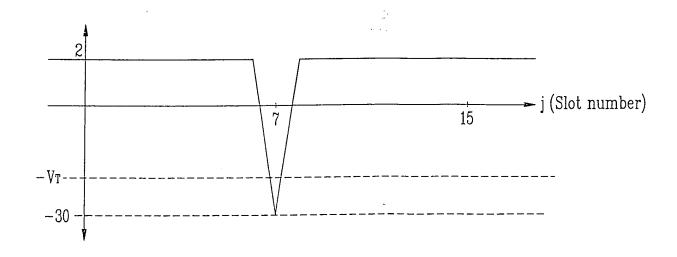


FIG.37

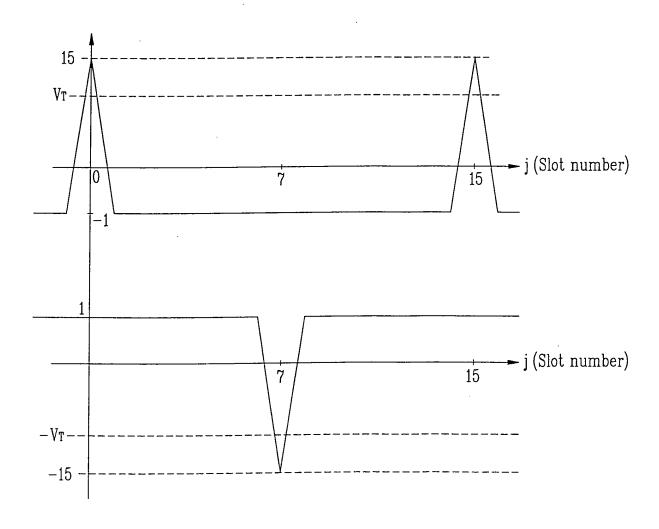


FIG. 38

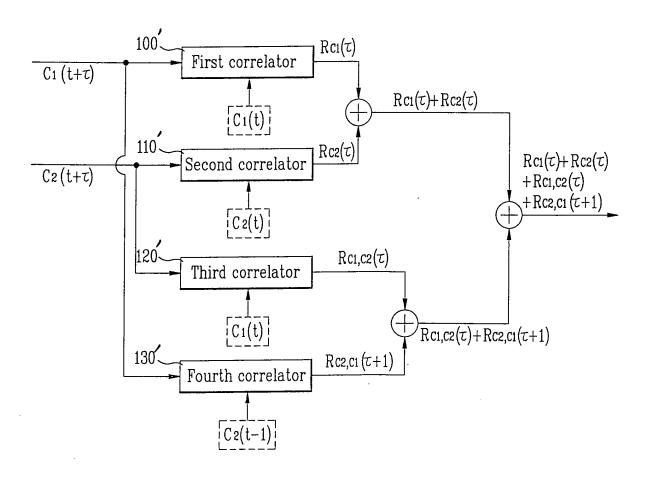


FIG. 39A

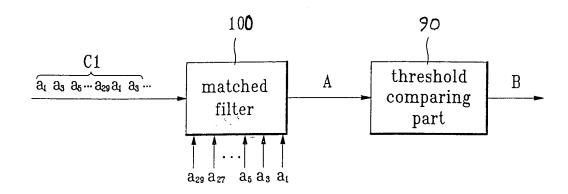
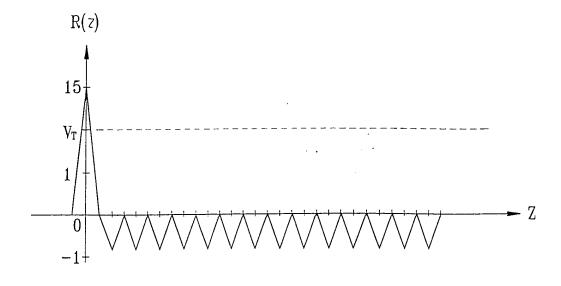


FIG. 39B



, 📜, ,

FIG. 40A

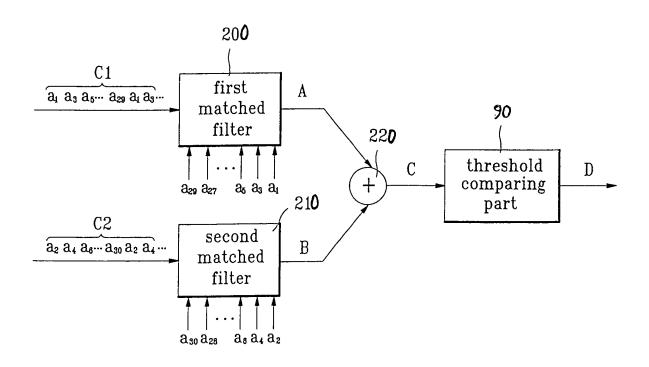


FIG. 40B

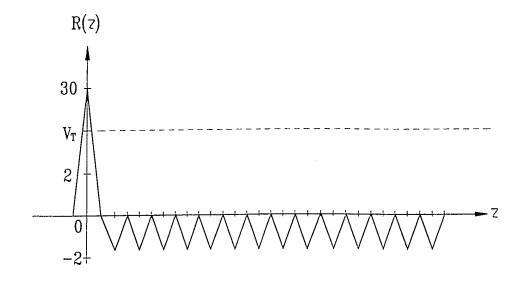


FIG. 41A

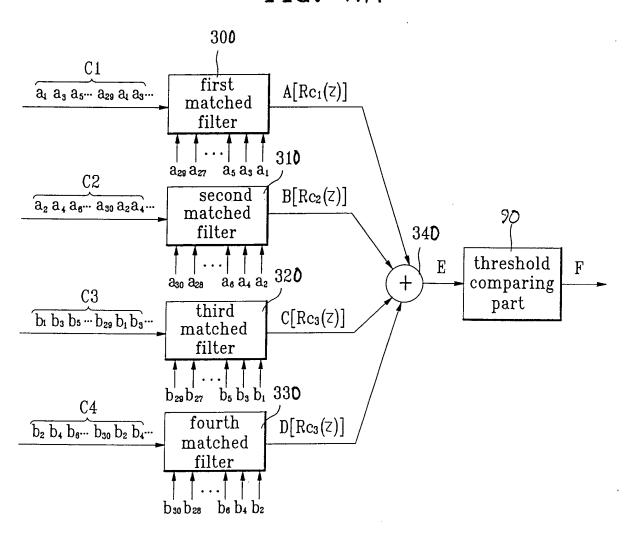


FIG. 41B

